POWER CONTROL OF A PROCESSOR USING

HARDWARE STRUCTURES CONTROLLED BY A COMPILER WITH

AN ACCUMULATED INSTRUCTION PROFILE

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ABSTRACT OF THE DISCLOSURE

A microprocessor includes a logic circuit. A selection device is coupled to the logic circuit, and the selection device provides switching of on/off states of the logic circuit based on a stored logical value. A program instruction is included which sets the stored logical value to control the on/off states of the logic circuit based on anticipated usage of the logical circuit in accordance with an instruction sequence of the microprocessor.

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